

Fig. 1A

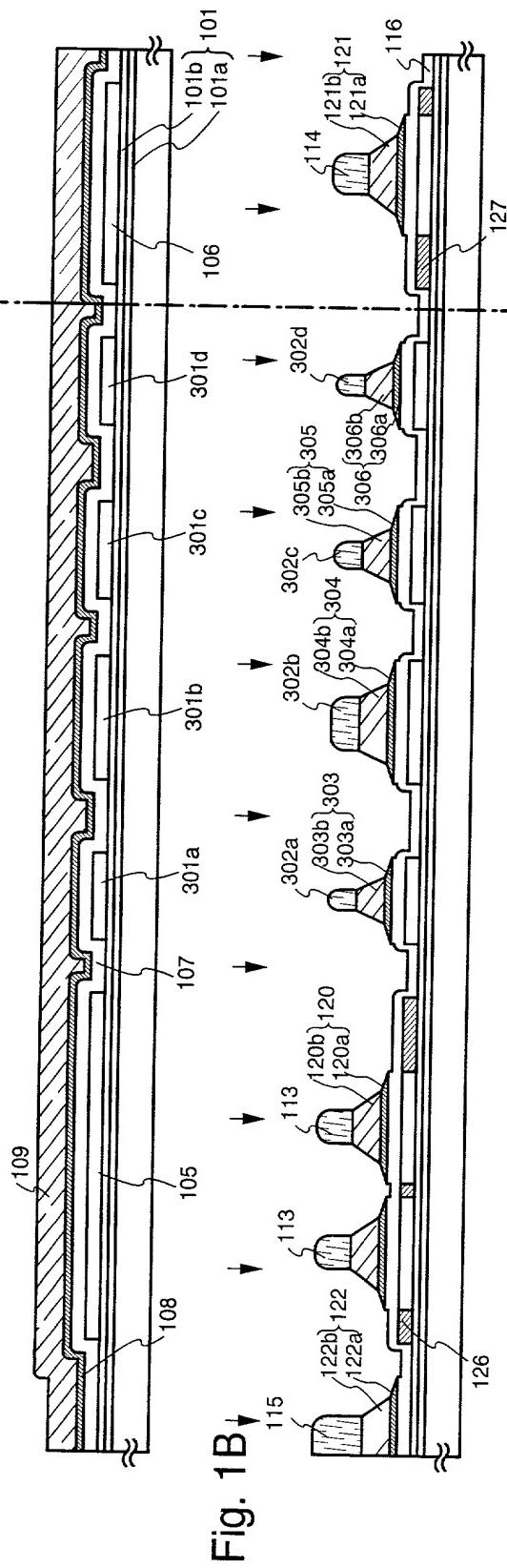


Fig. 1C

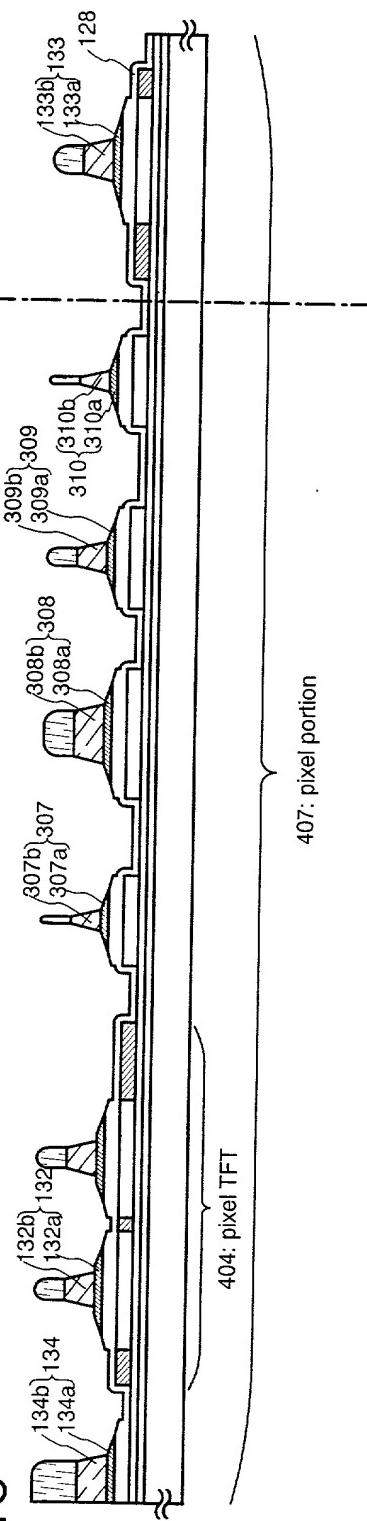


Fig. 2A

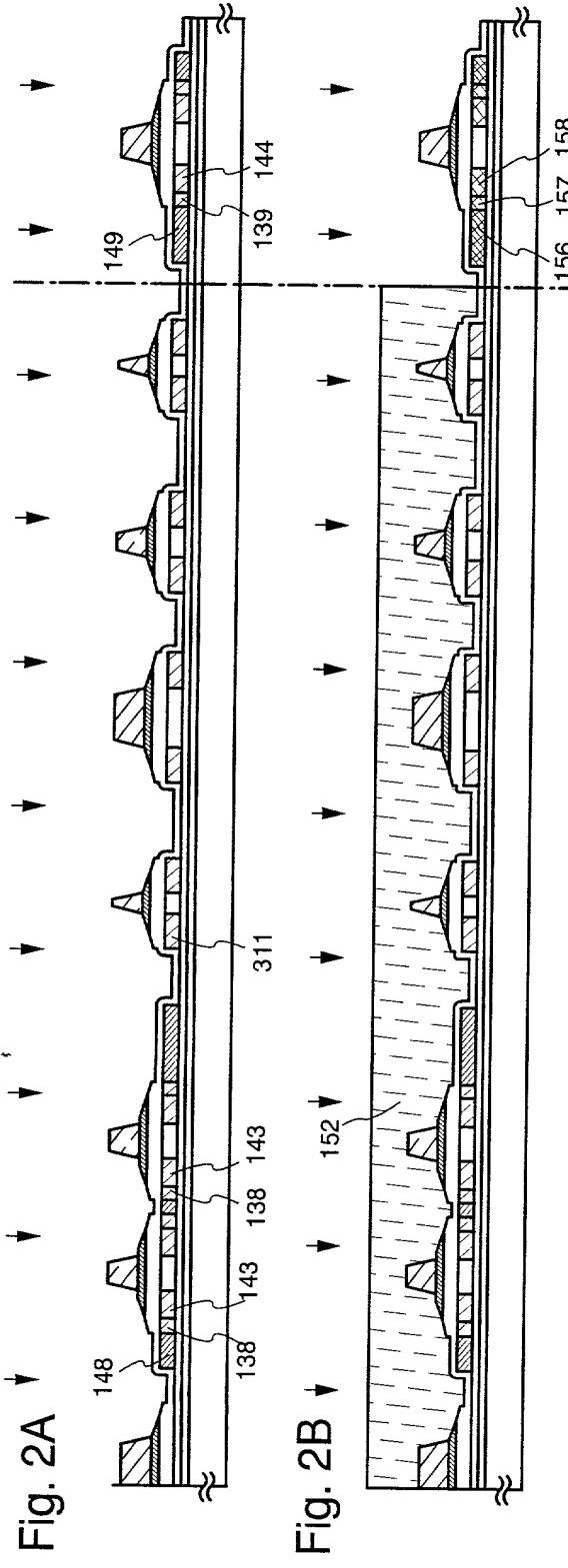


Fig. 2B

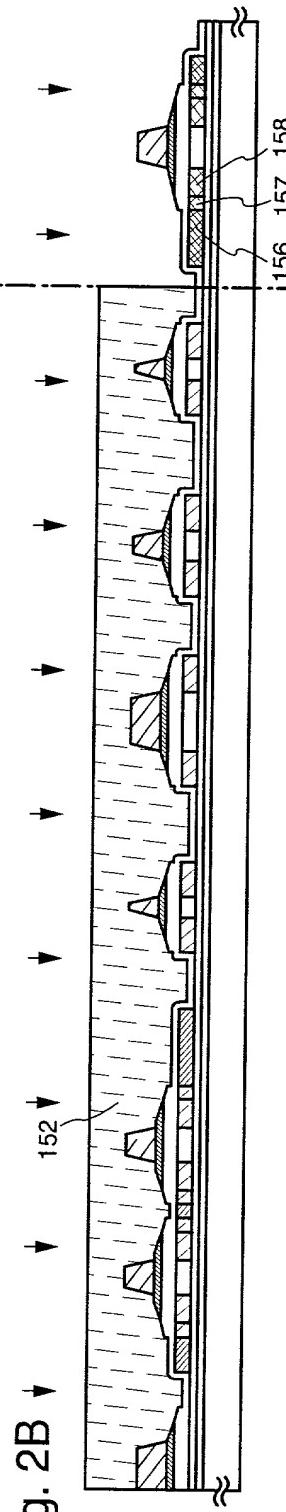
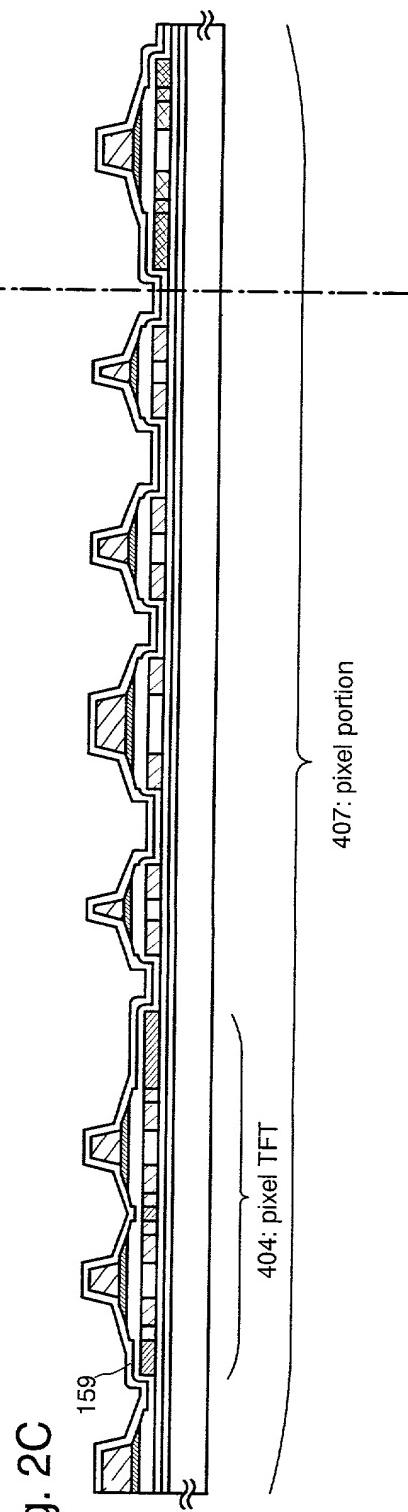


Fig. 2C



407: pixel portion

404: pixel TFT

Fig. 3

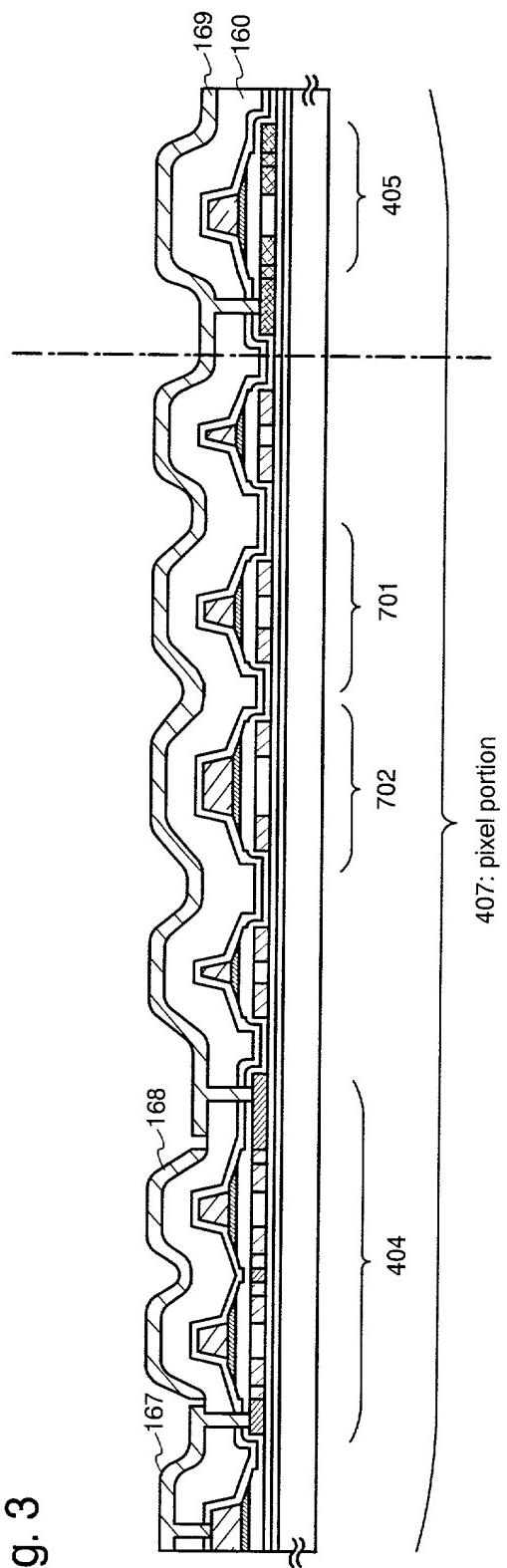


Fig. 4

incident light

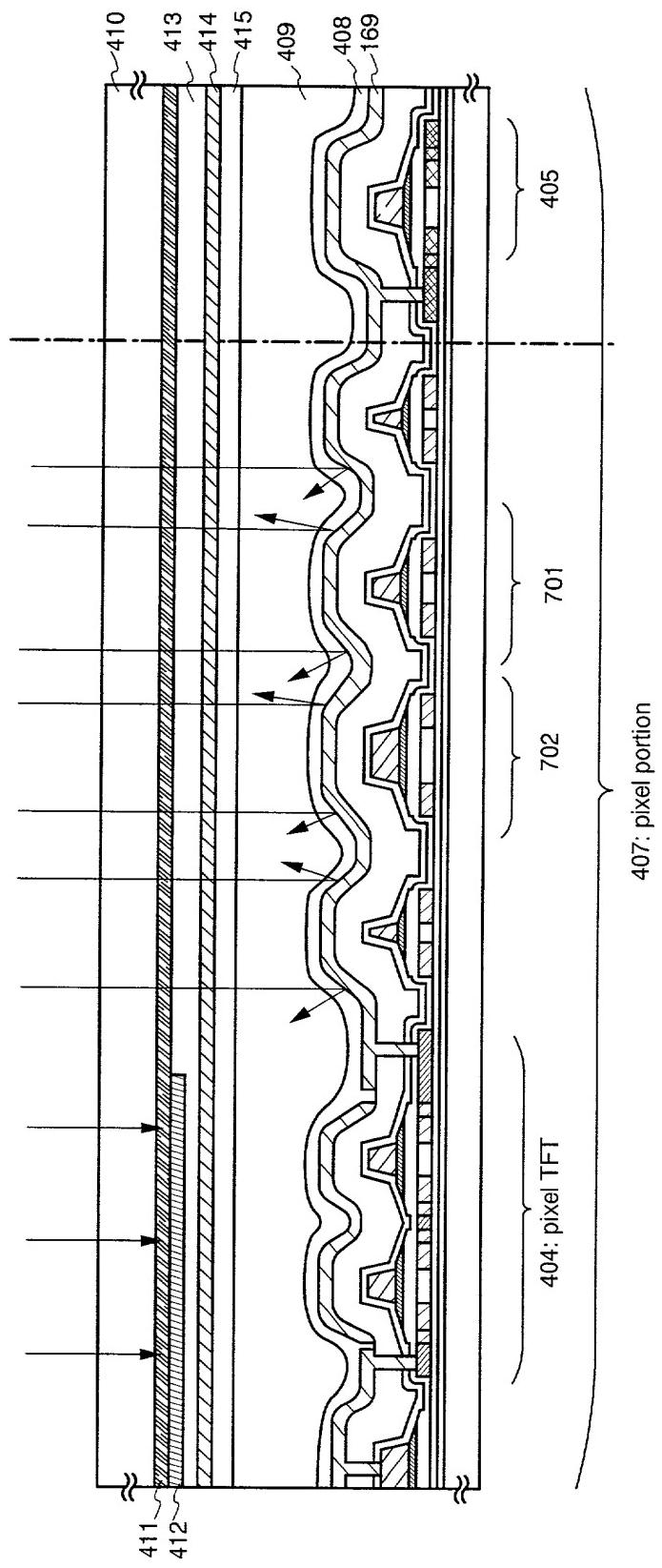
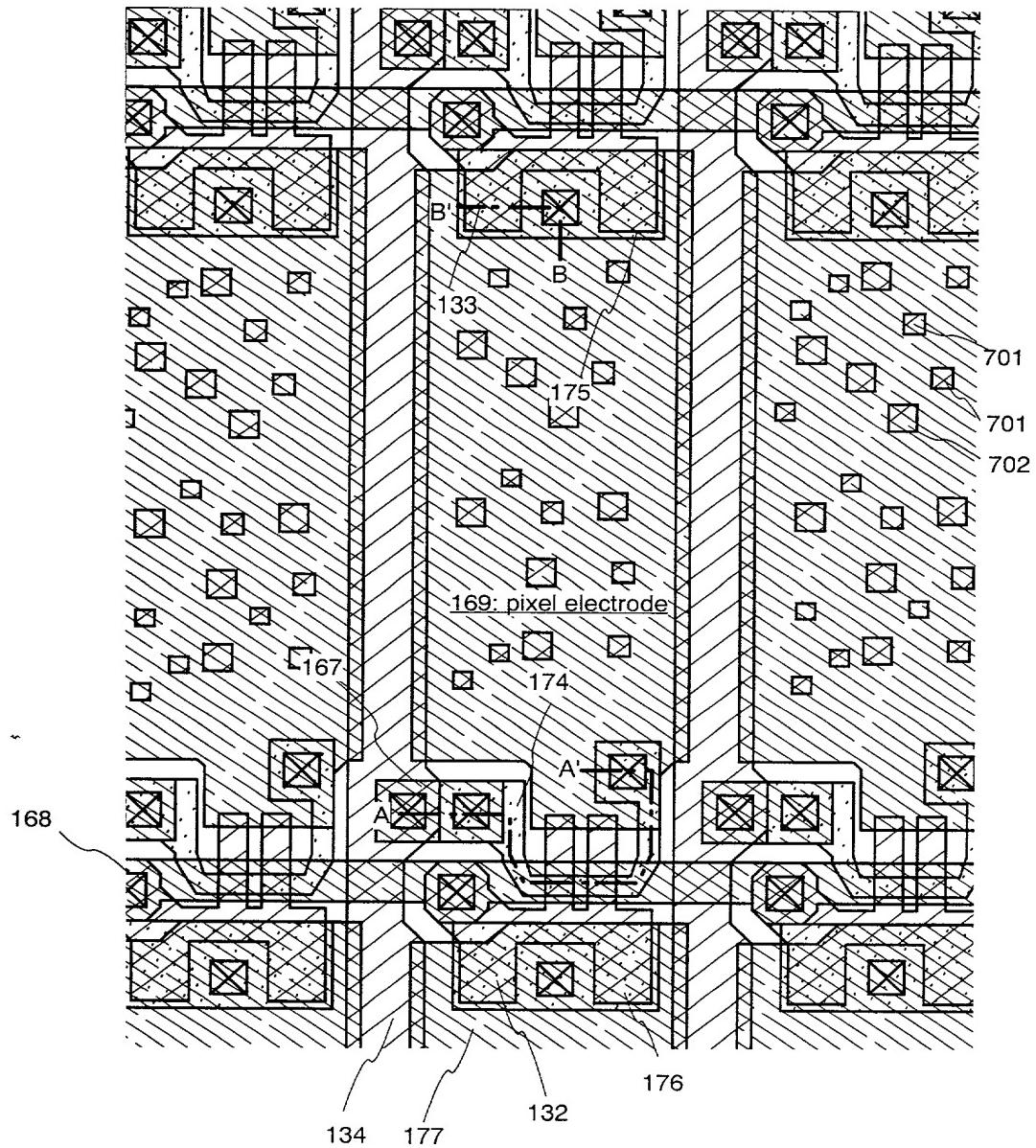


Fig. 5



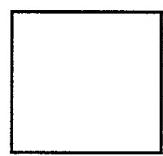


Fig. 6A

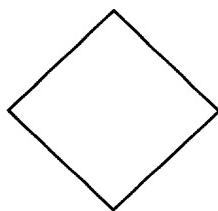


Fig. 6B



Fig. 6C

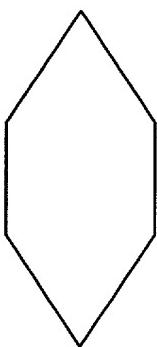


Fig. 6D

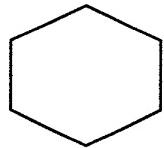


Fig. 6E

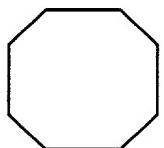


Fig. 6F

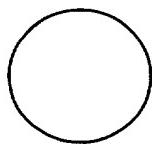
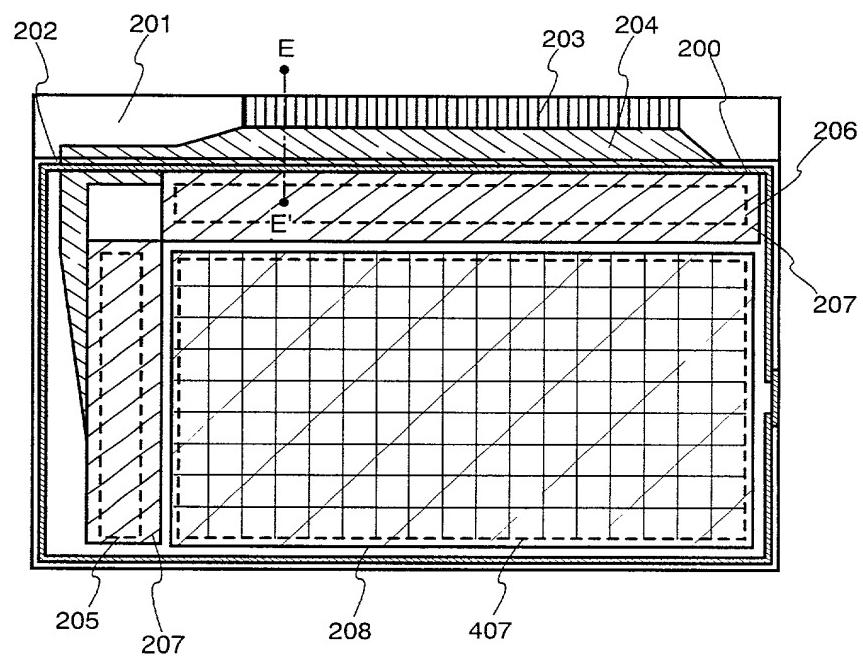


Fig. 6G

Fig. 7



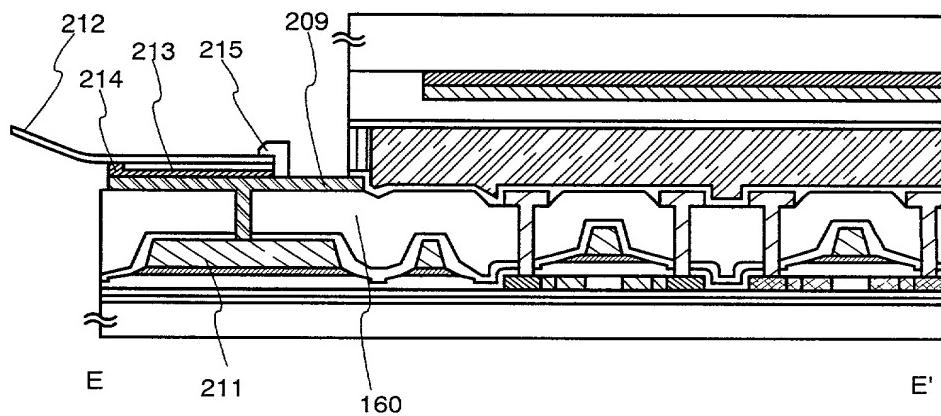


Fig. 8A cross sectional view of E-E'

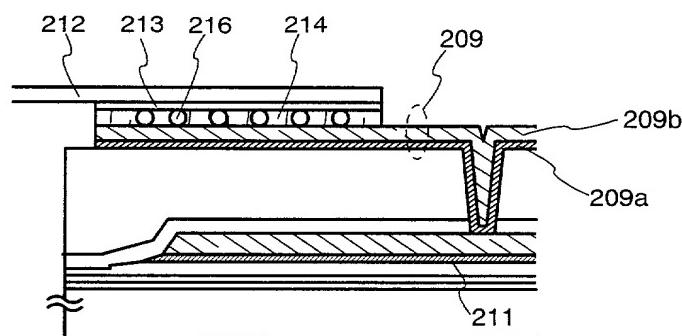


Fig. 8B

Fig. 9A

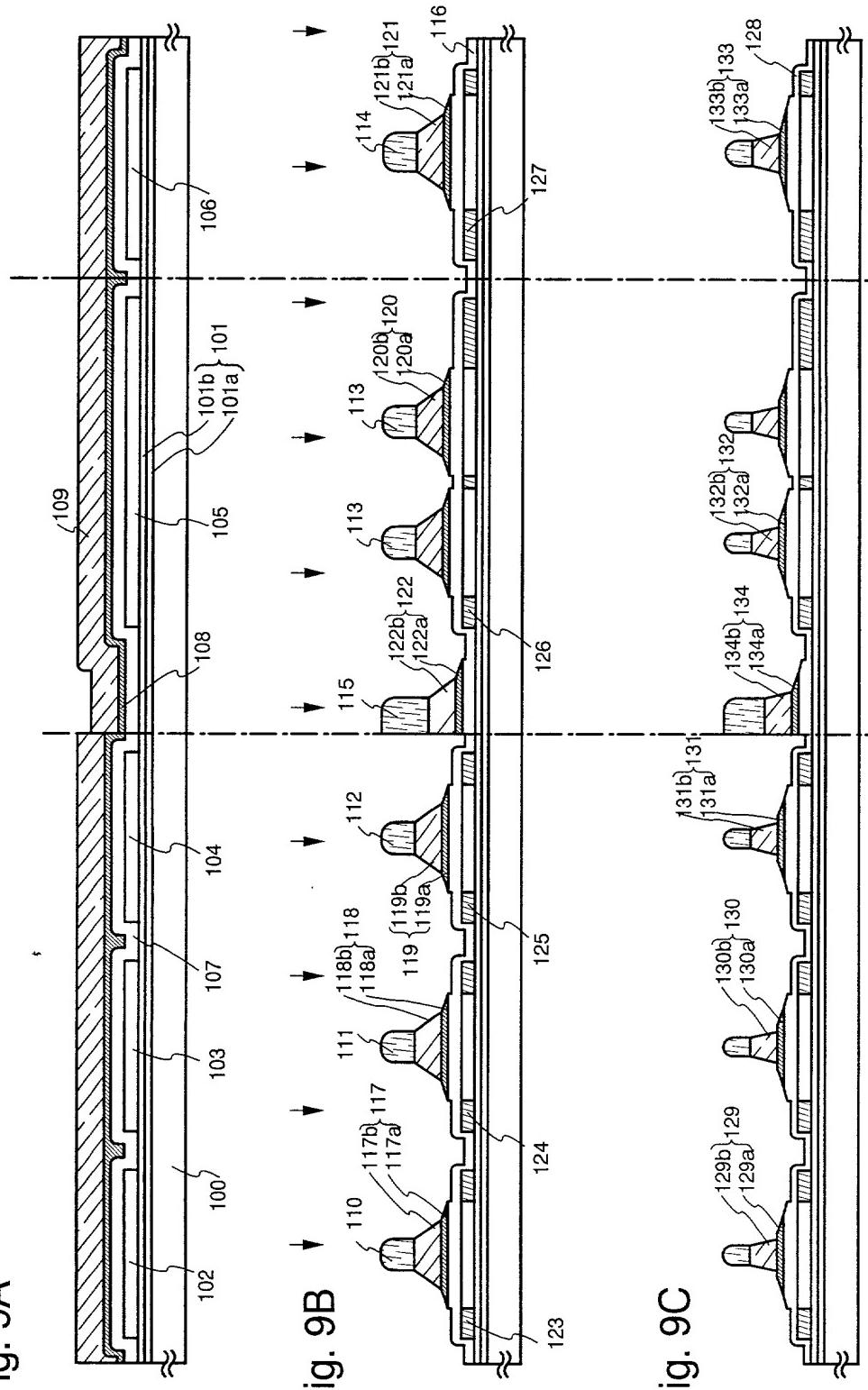


Fig. 10A

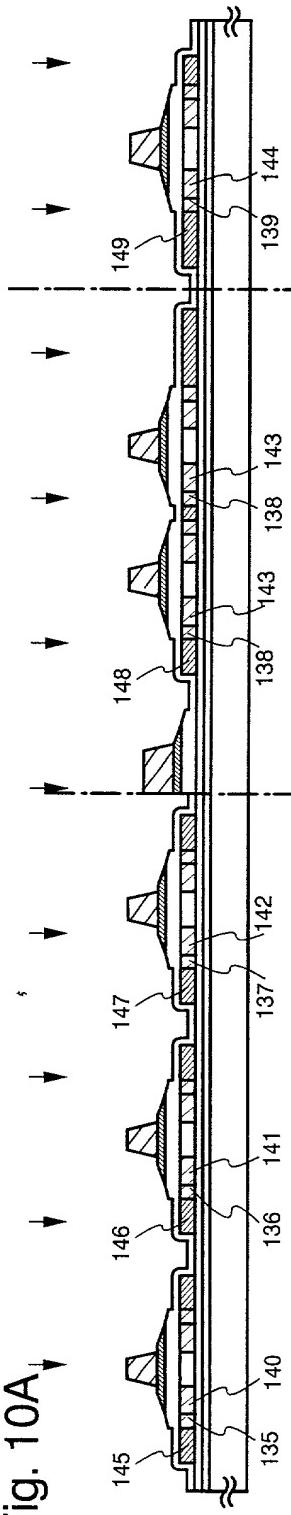


Fig. 10B

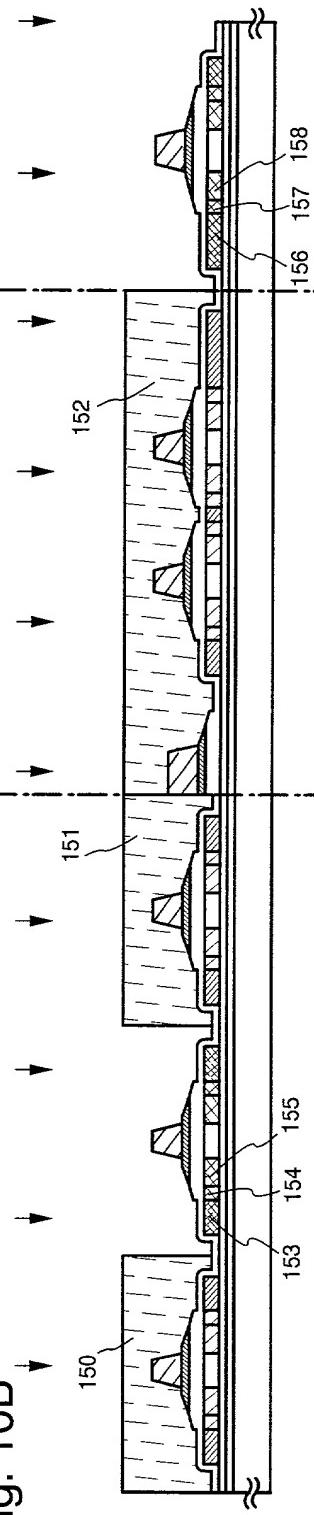


Fig. 10C

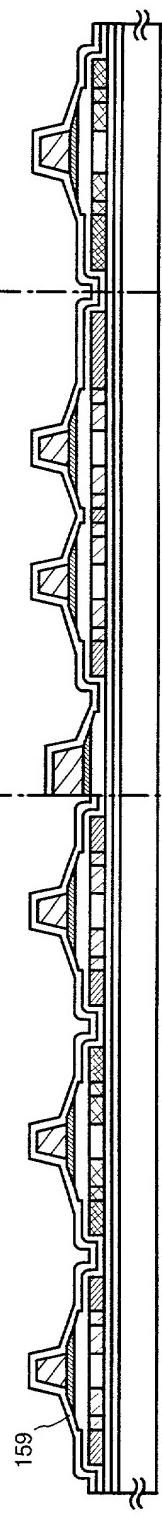


Fig. 11

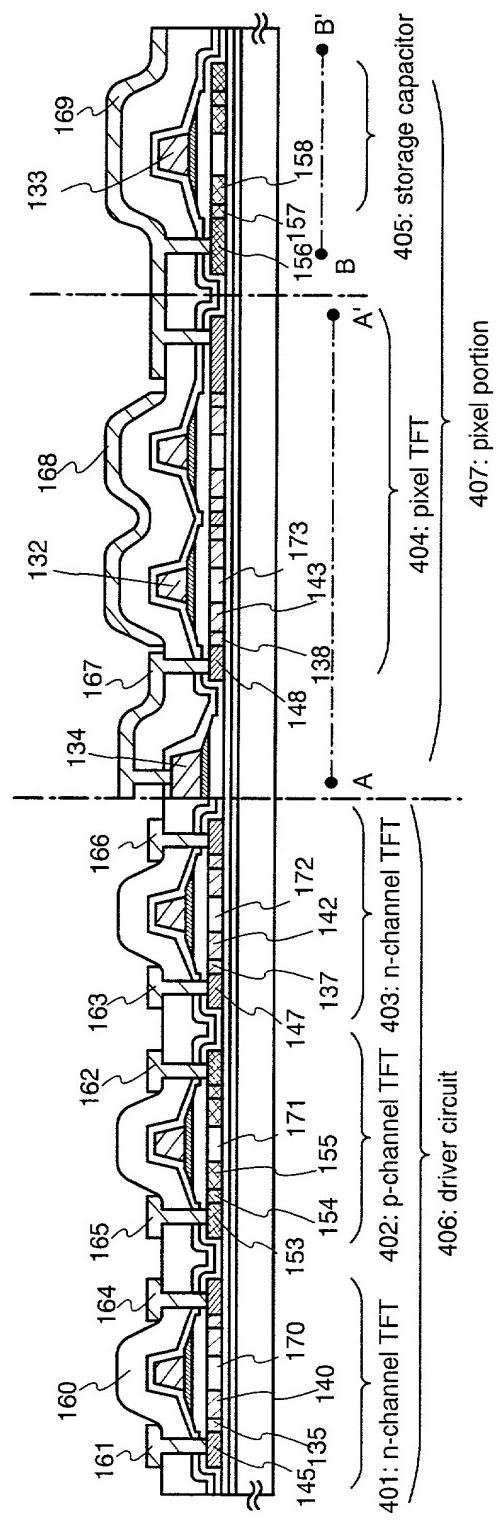
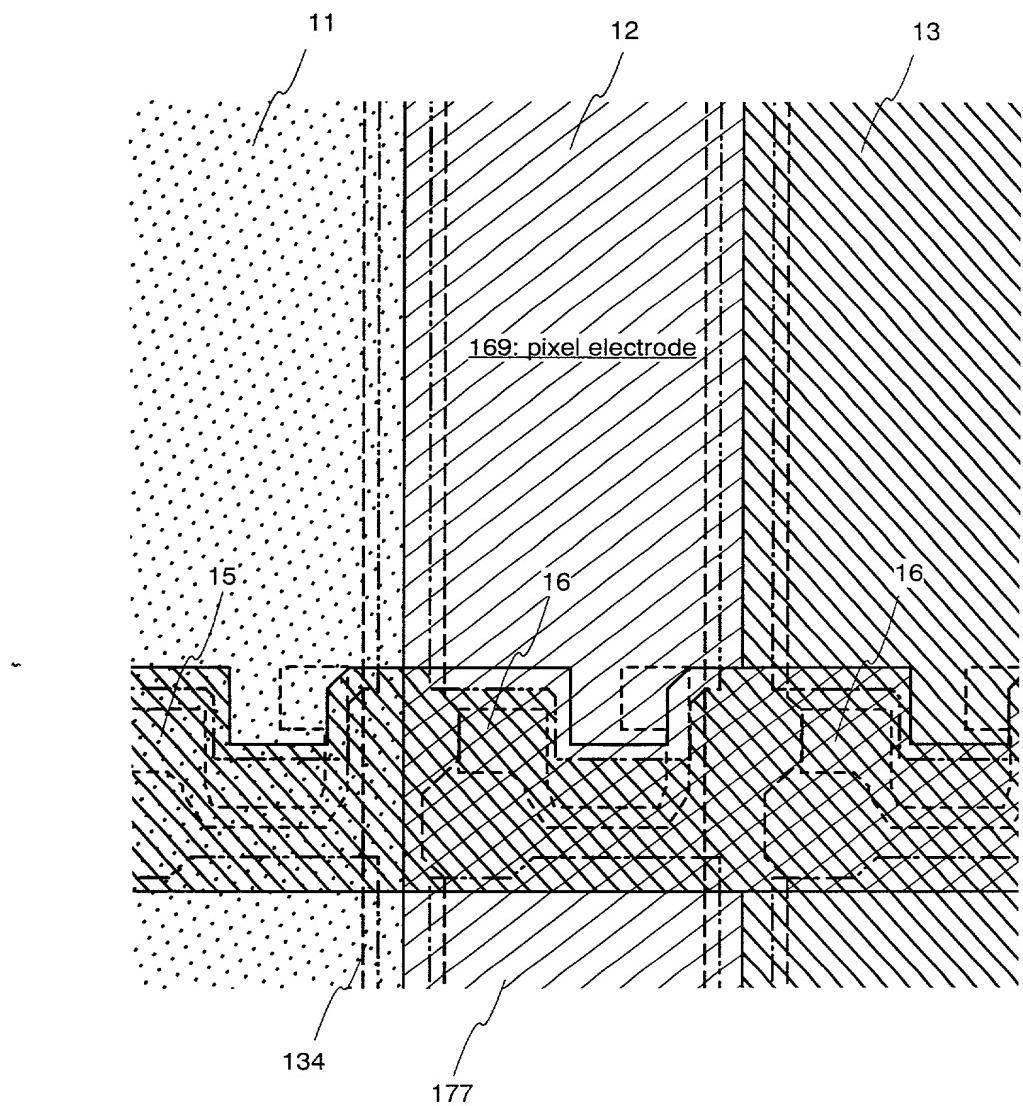


Fig. 12



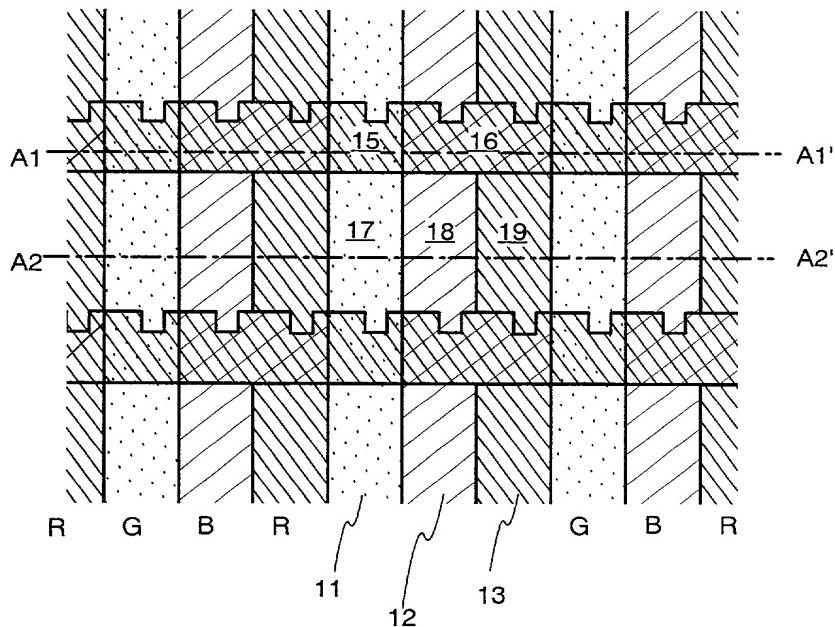


Fig. 13A

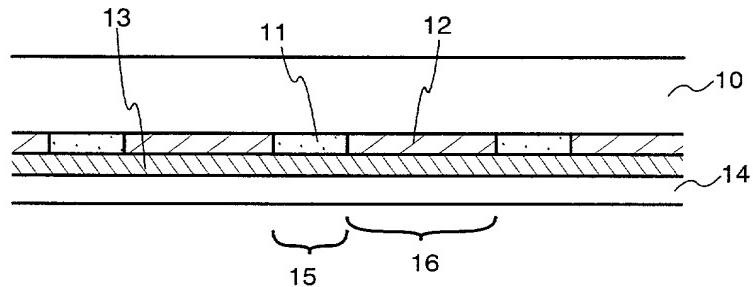


Fig. 13B cross sectional view of A1-A1'

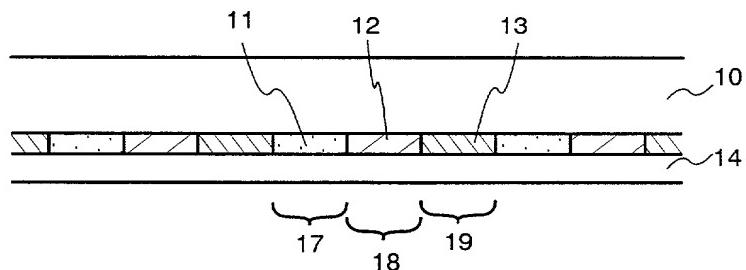


Fig. 13C cross sectional view of A2-A2'

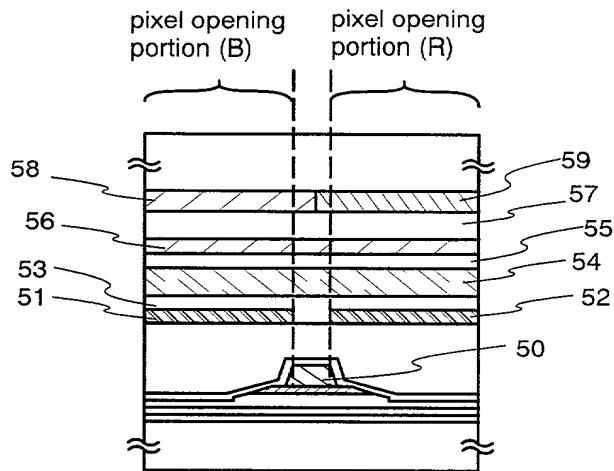


Fig. 14A

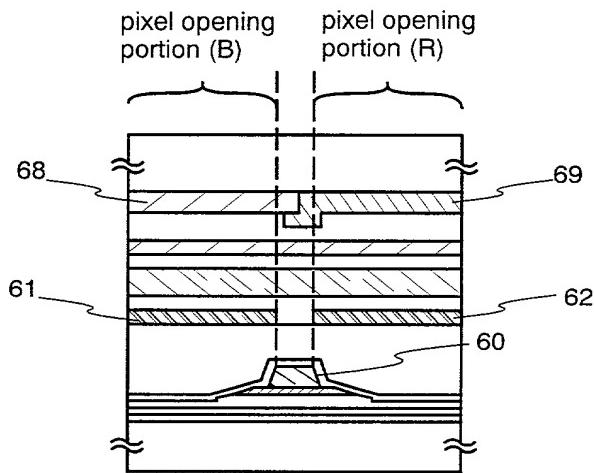


Fig. 14B

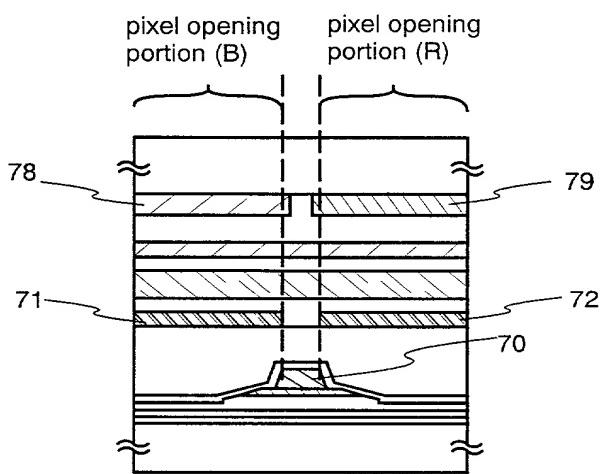


Fig. 14C

Fig. 15A

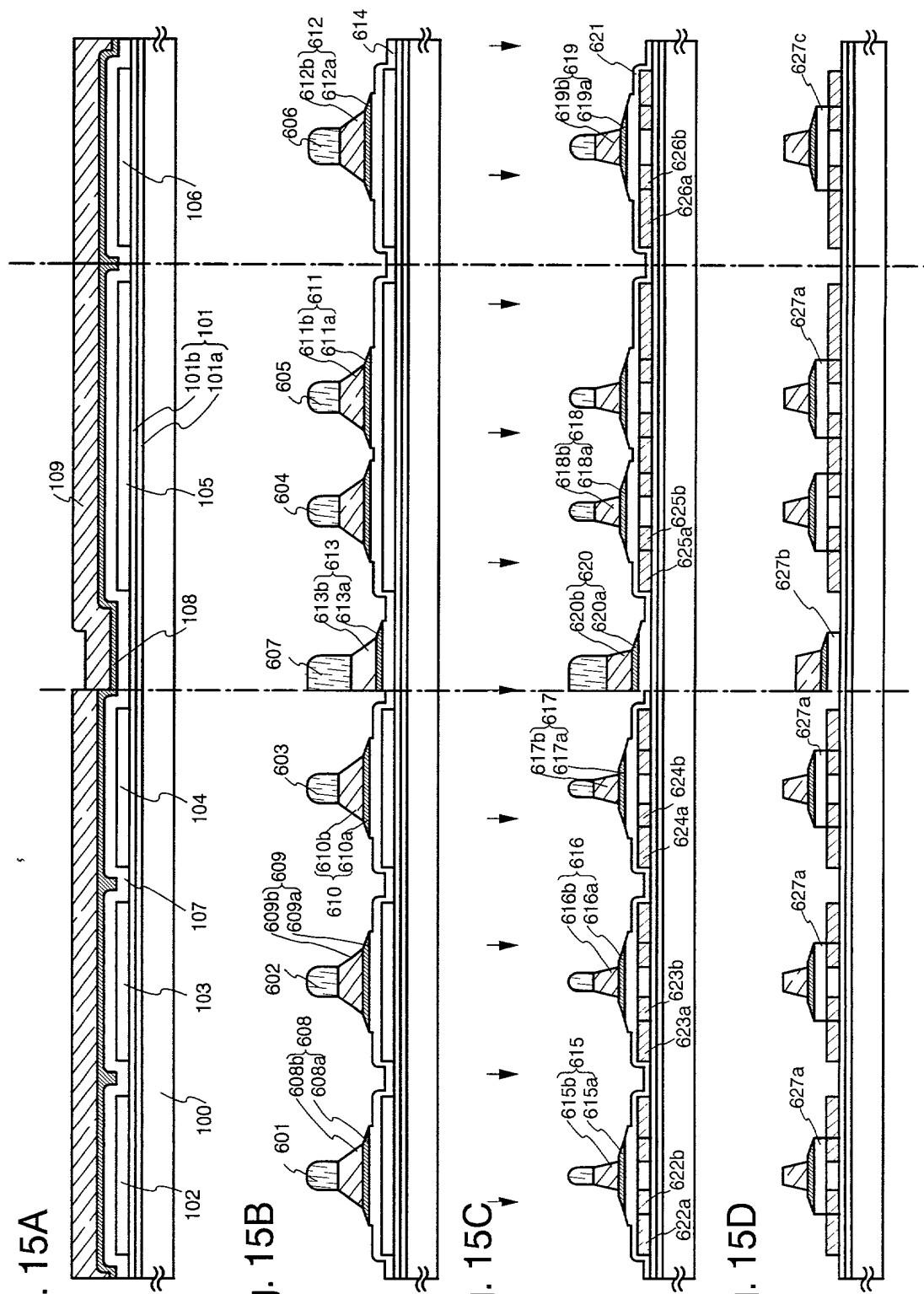


Fig. 16A

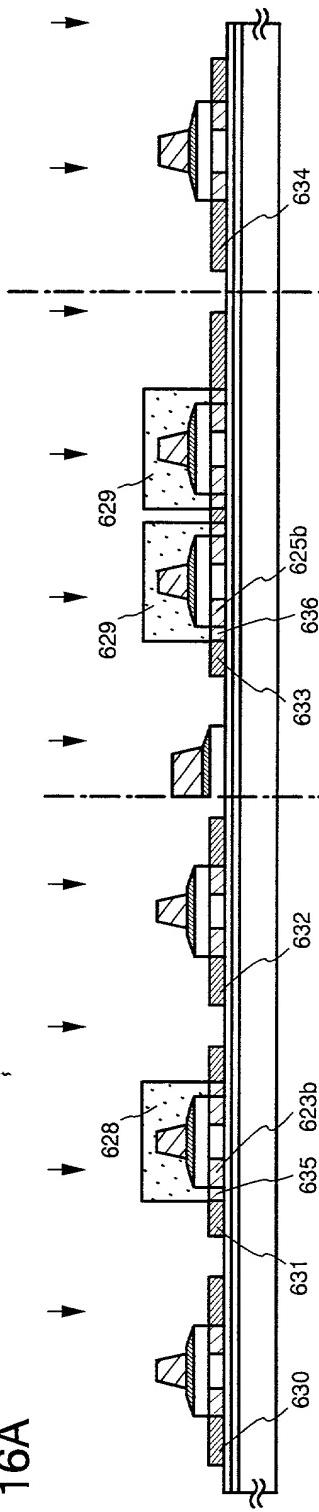


Fig. 16B

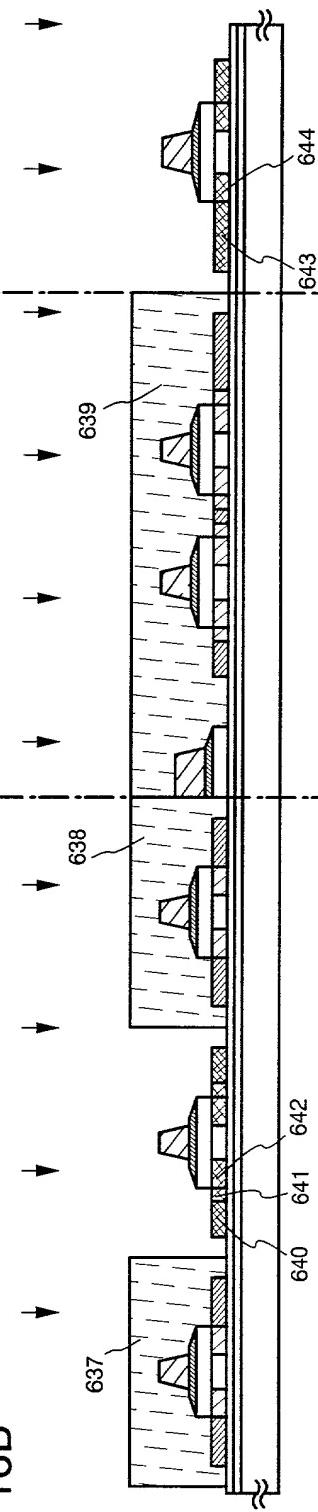


Fig. 16C

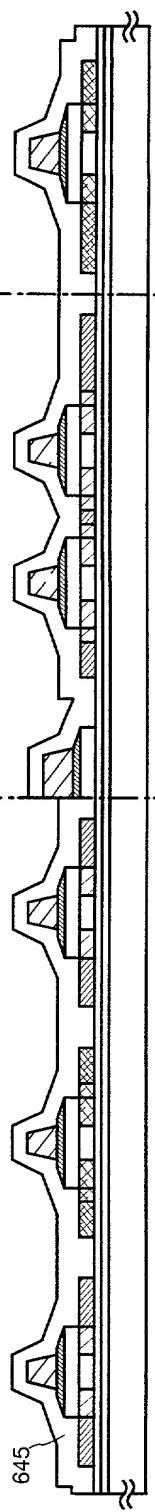


Fig. 17

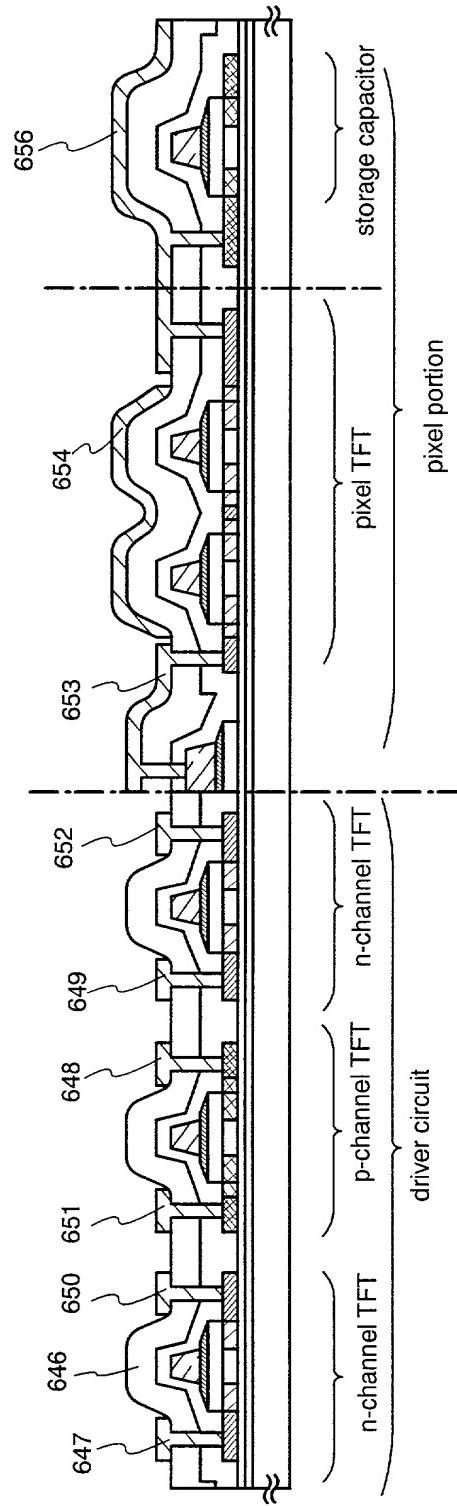


Fig. 18A

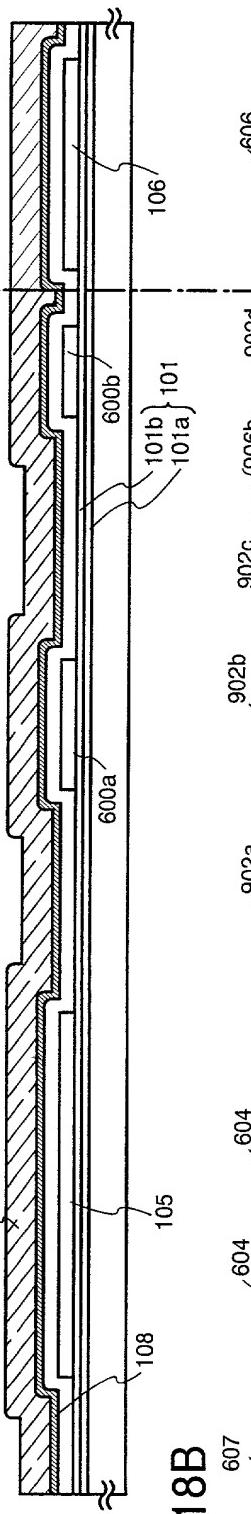


Fig. 18B

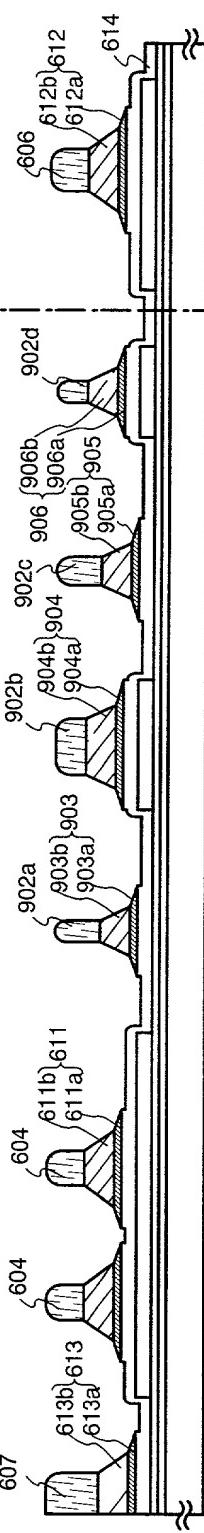


Fig. 18C

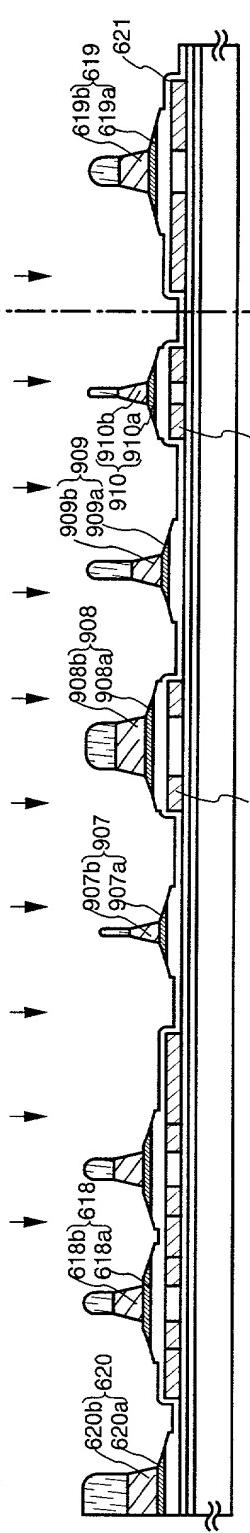
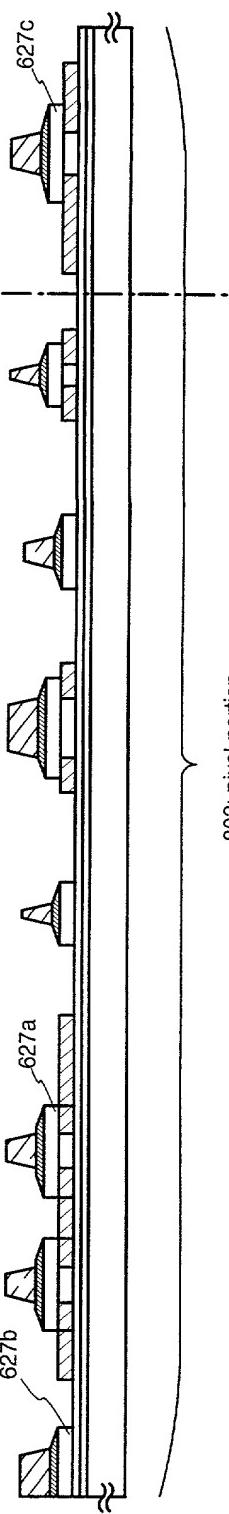


Fig. 18D



803: pixel portion

Fig. 19A

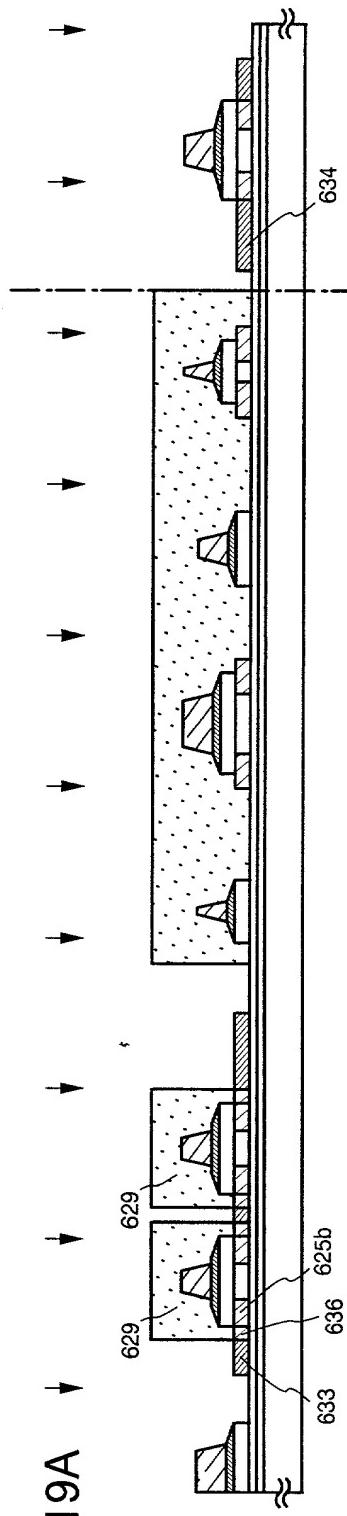


Fig. 19B

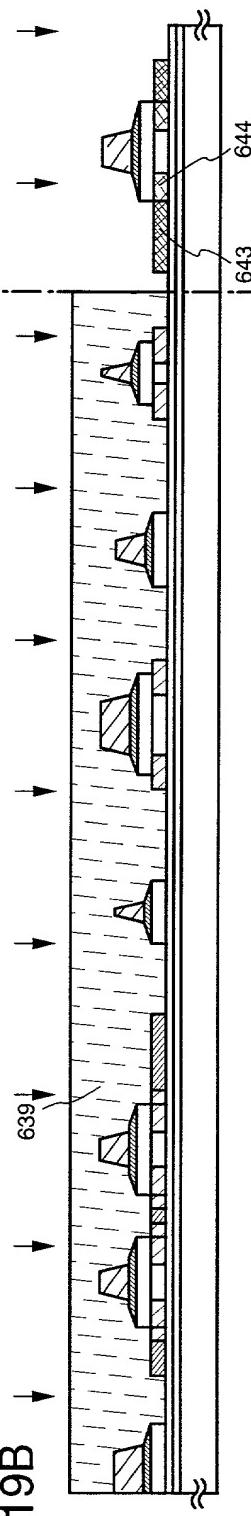


Fig. 19C

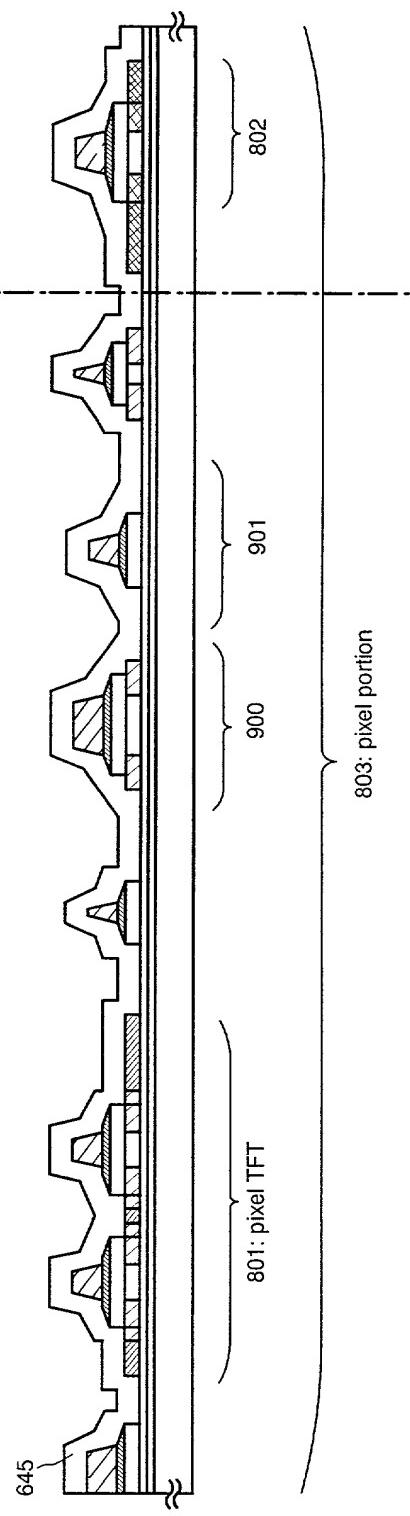


Fig. 20

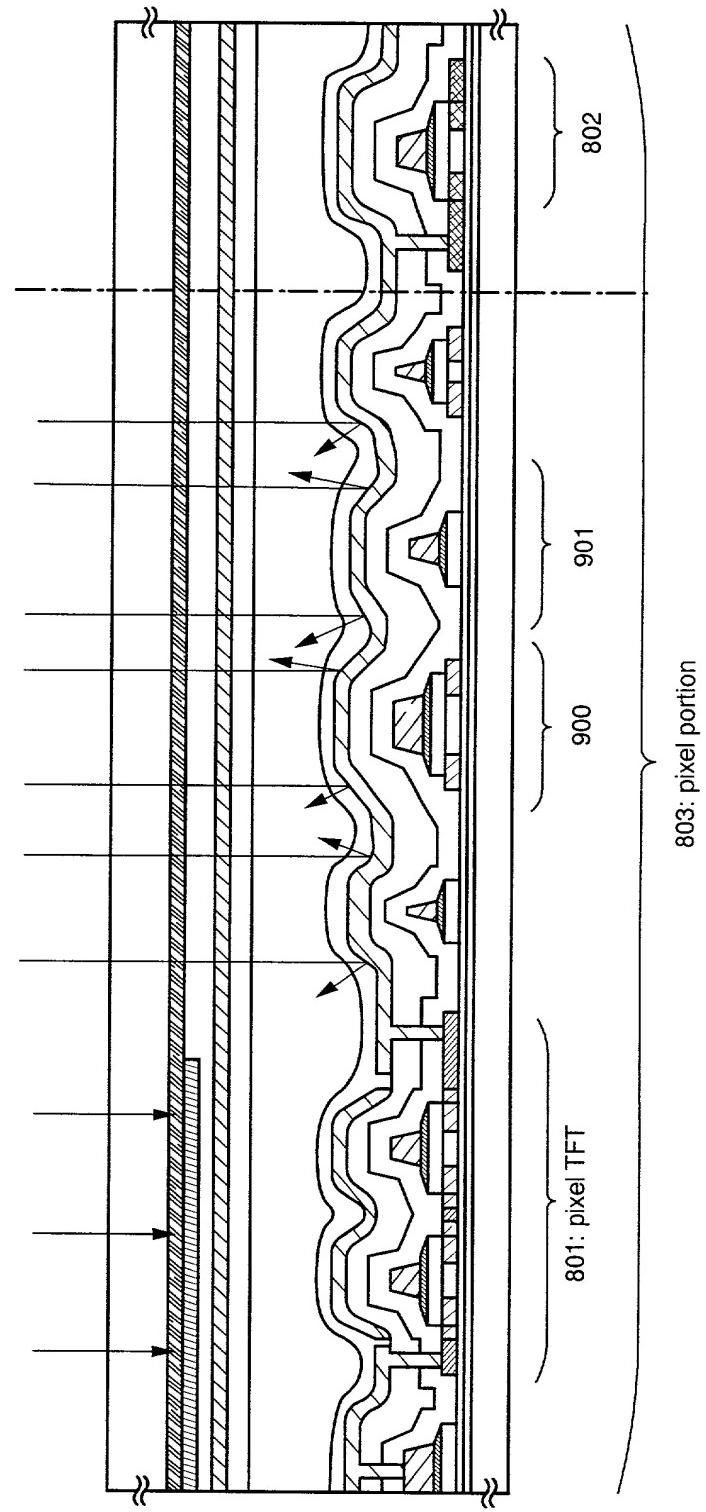


Fig. 21A

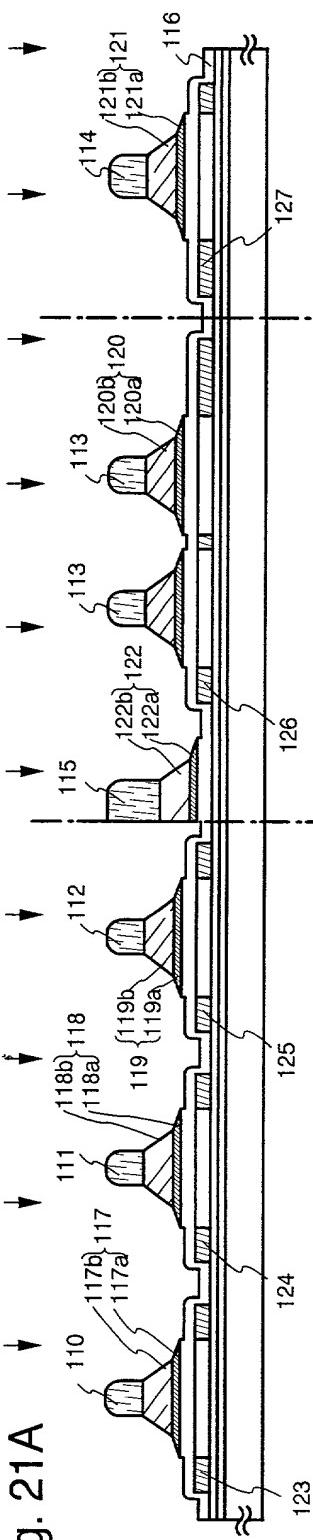


Fig. 21B

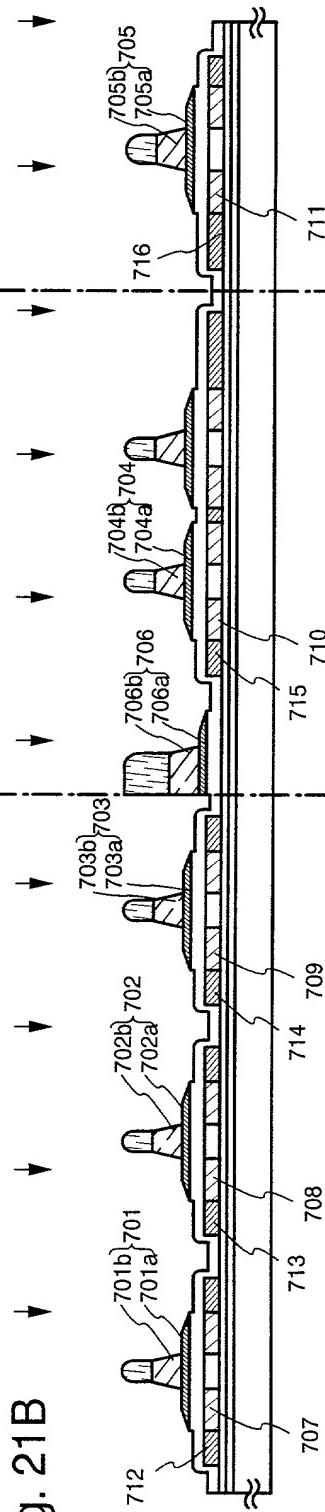


Fig. 21C

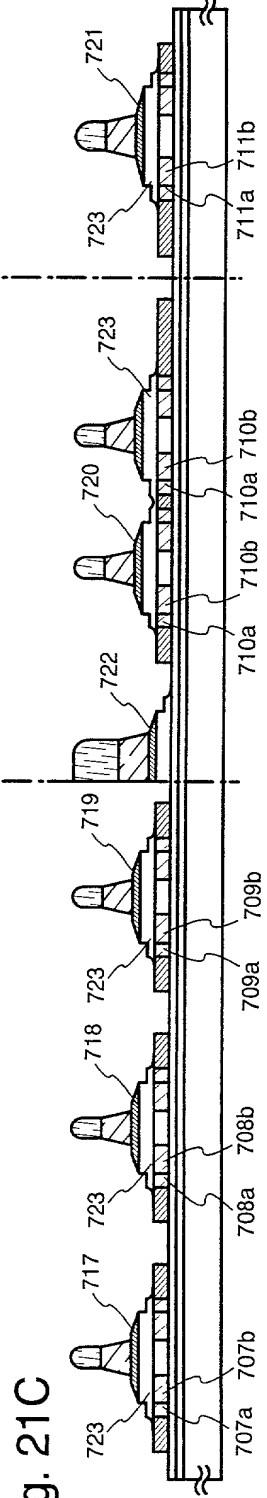


Fig. 22A

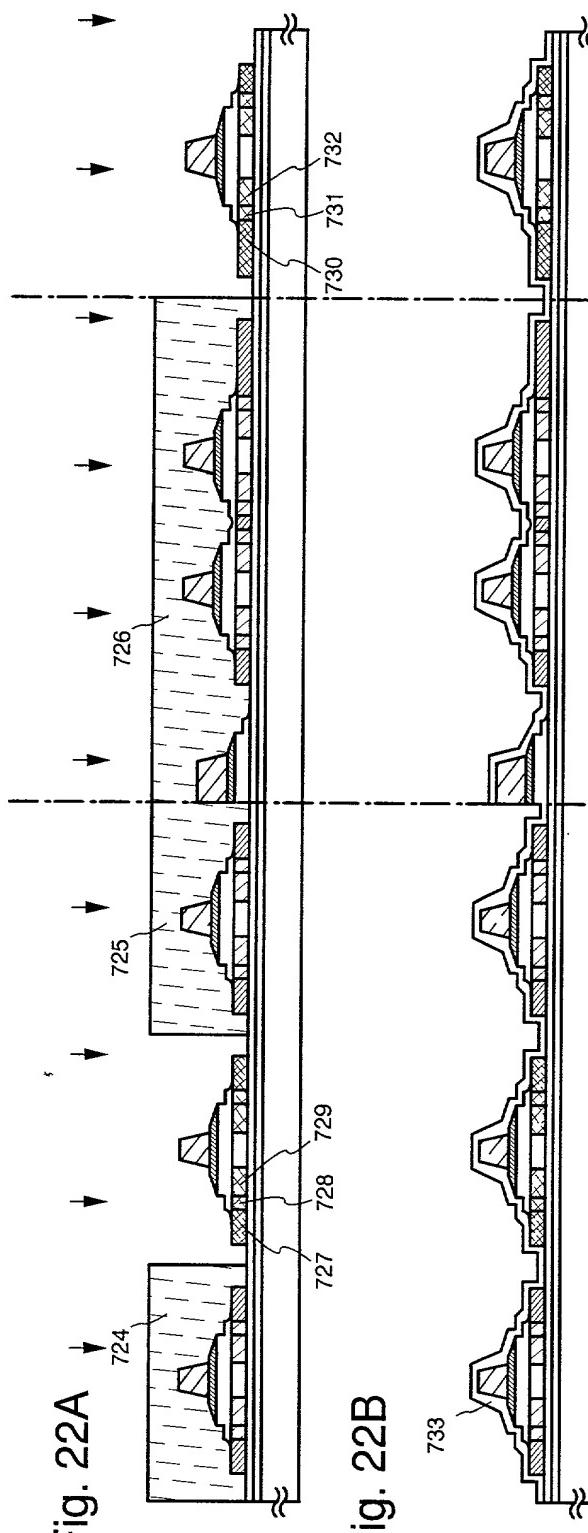


Fig. 22B

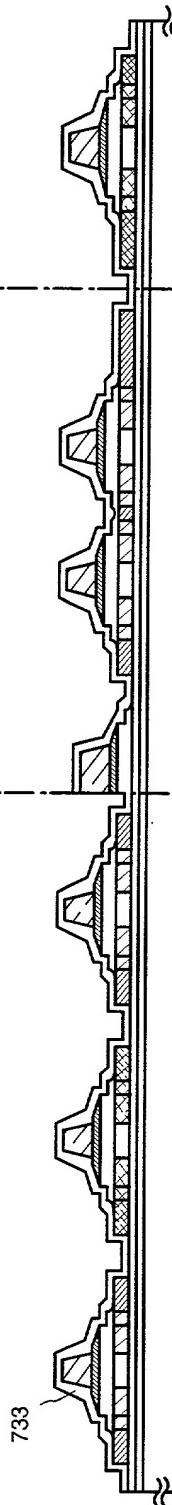
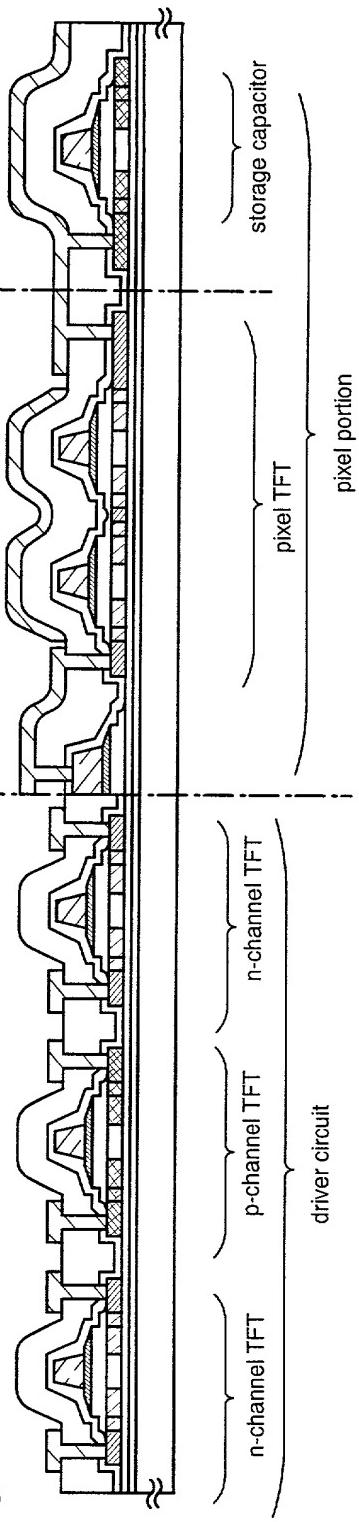


Fig. 22C



n-channel TFT

p-channel TFT

n-channel TFT

pixel portion

storage capacitor

pixel portion

Fig. 23

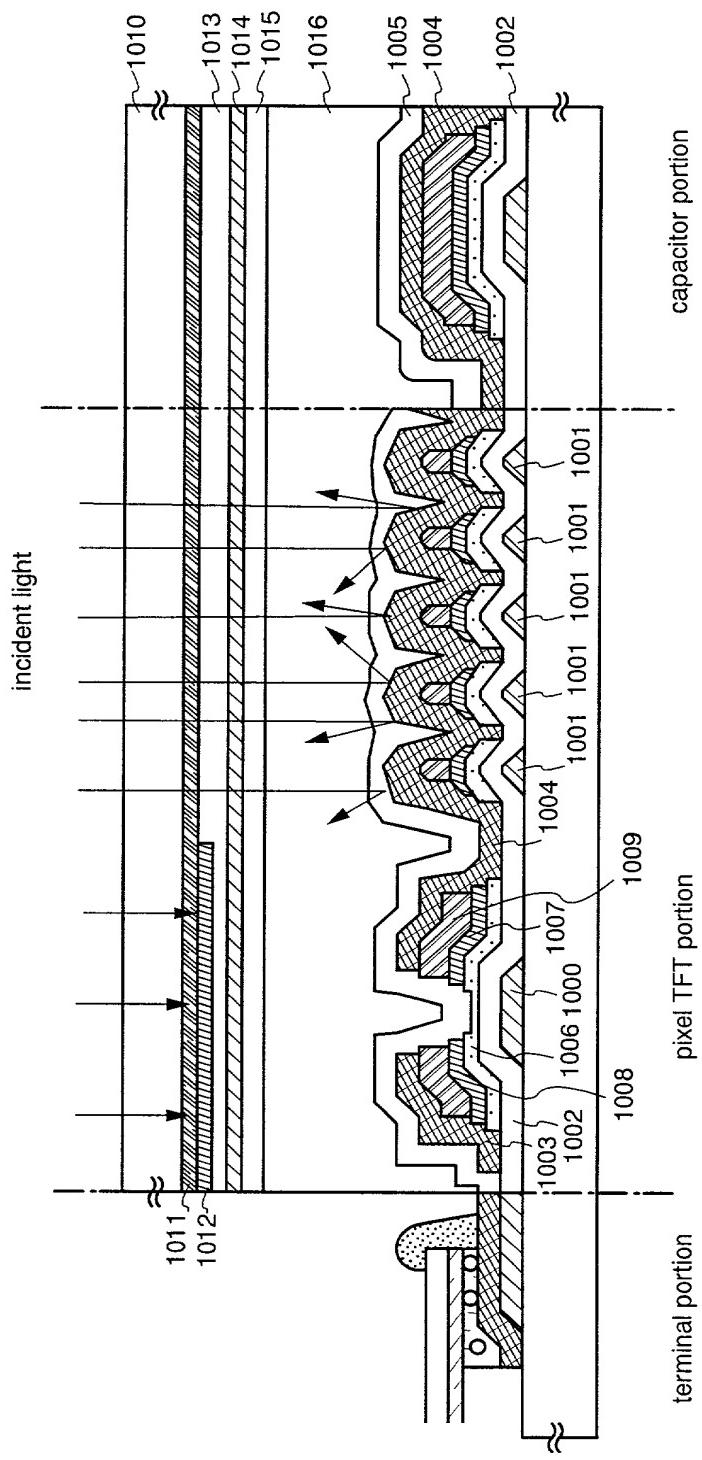


Fig. 24

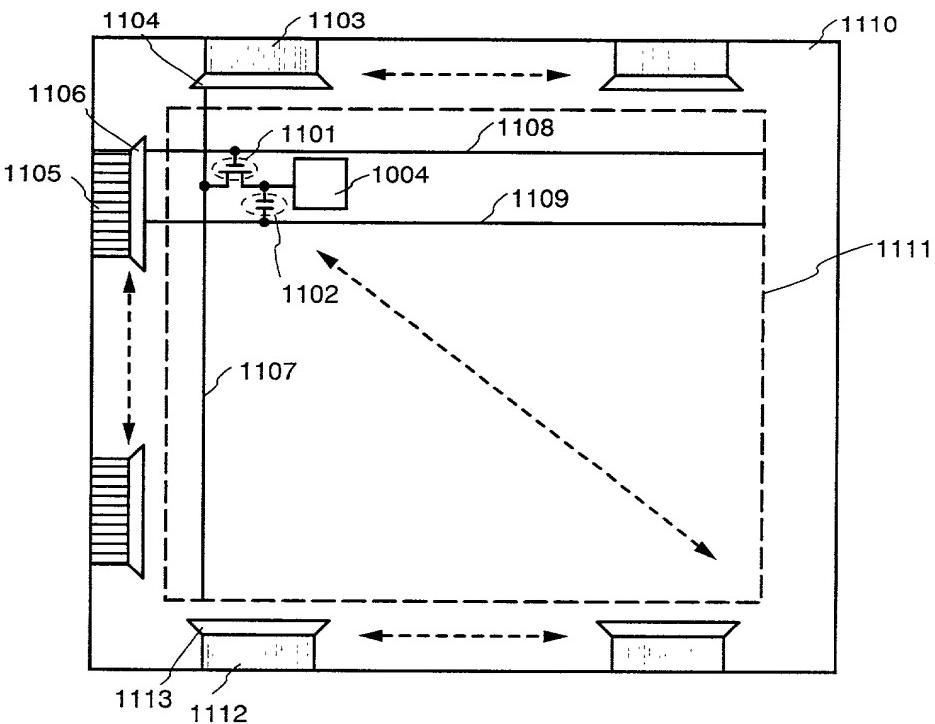


Fig. 25

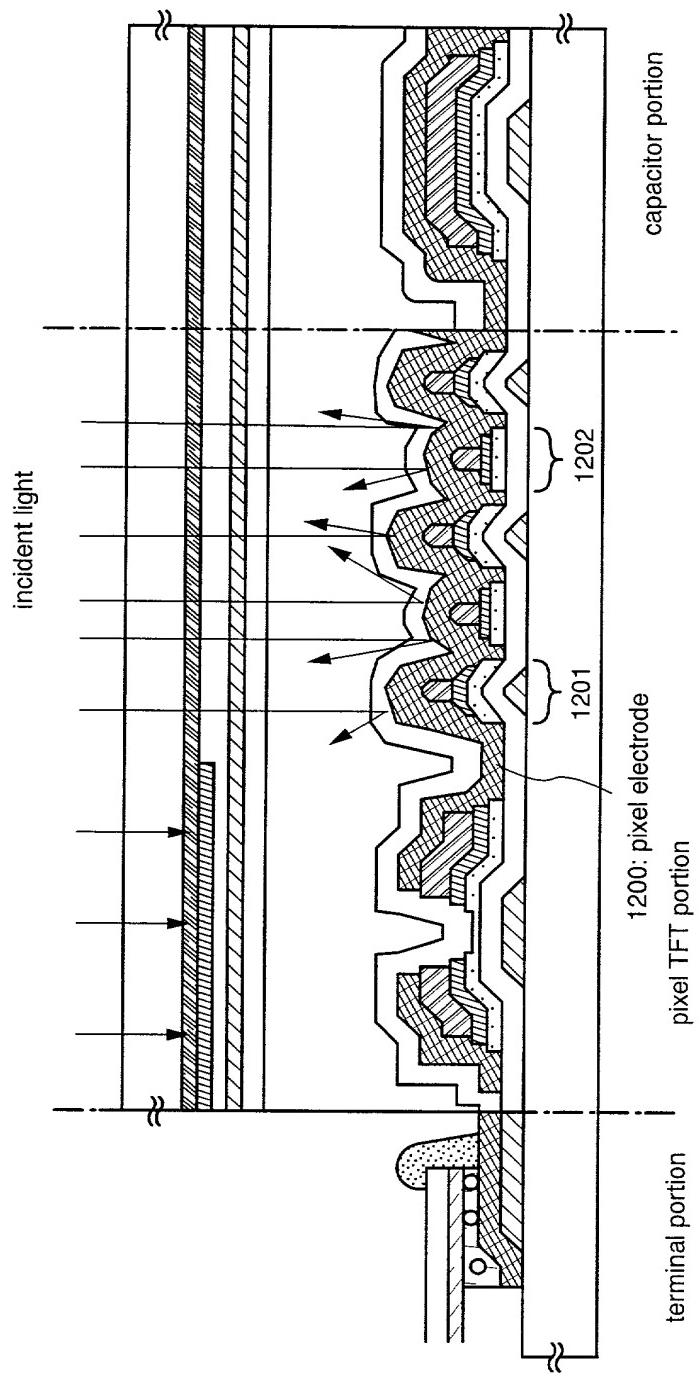
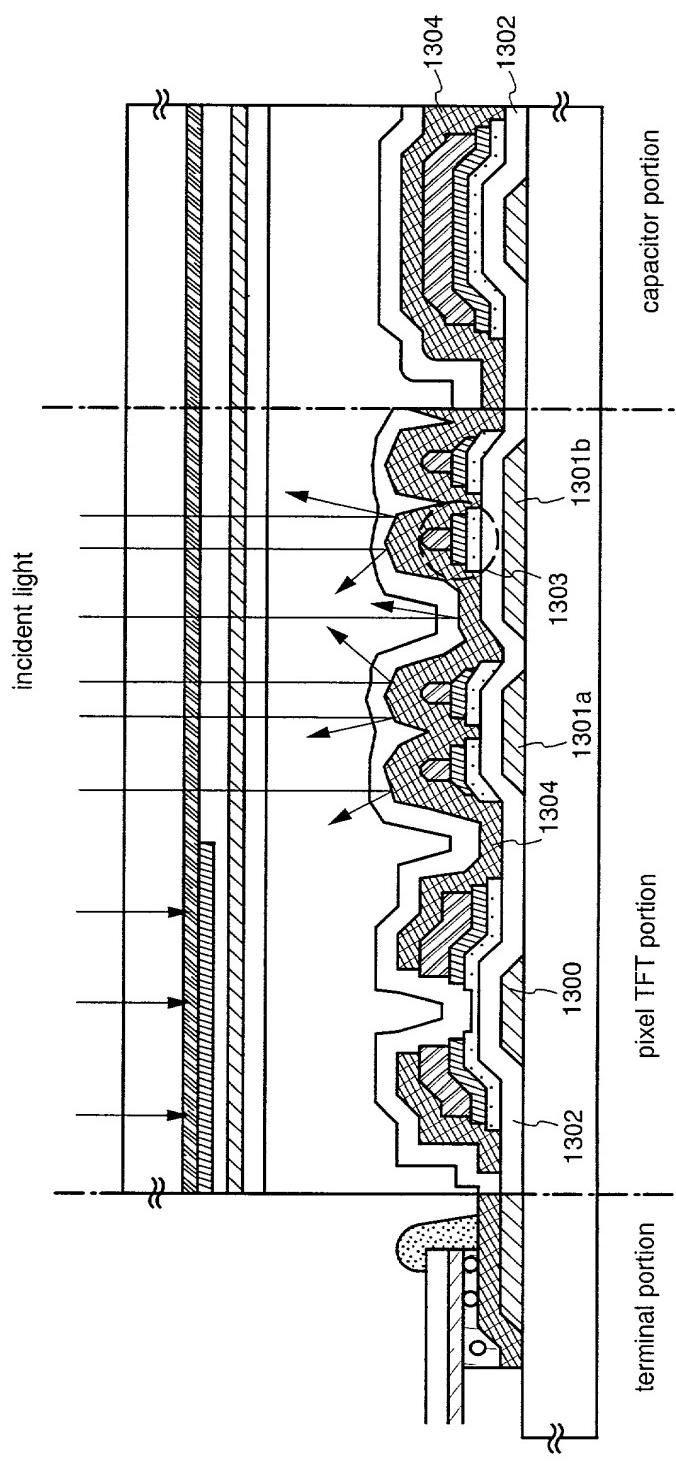
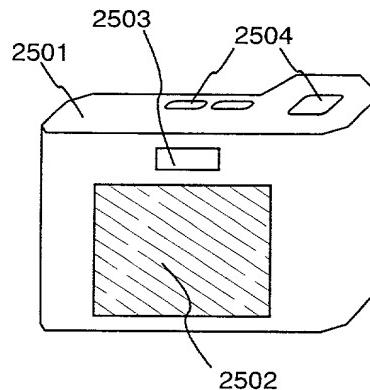
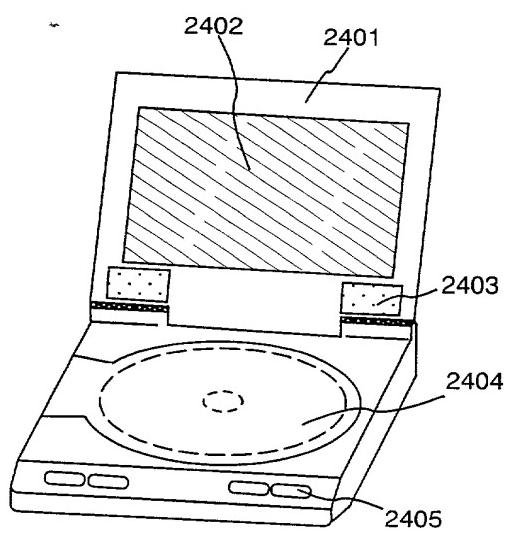
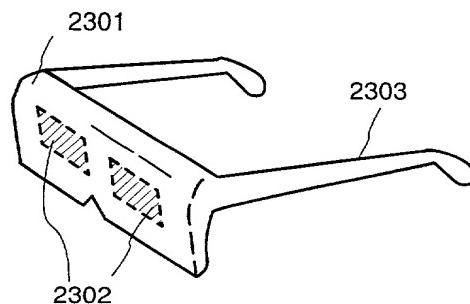
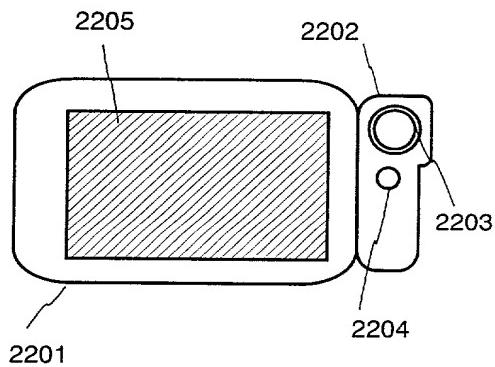
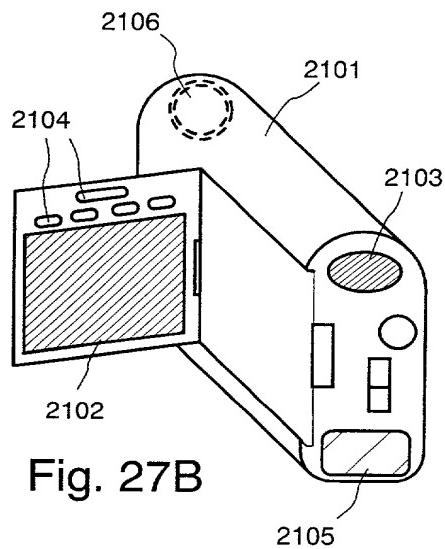
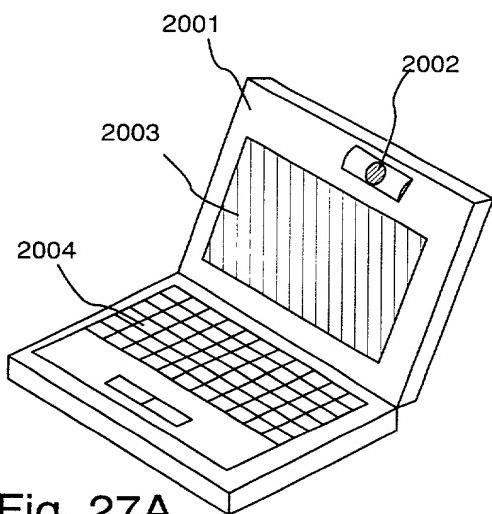


Fig. 26





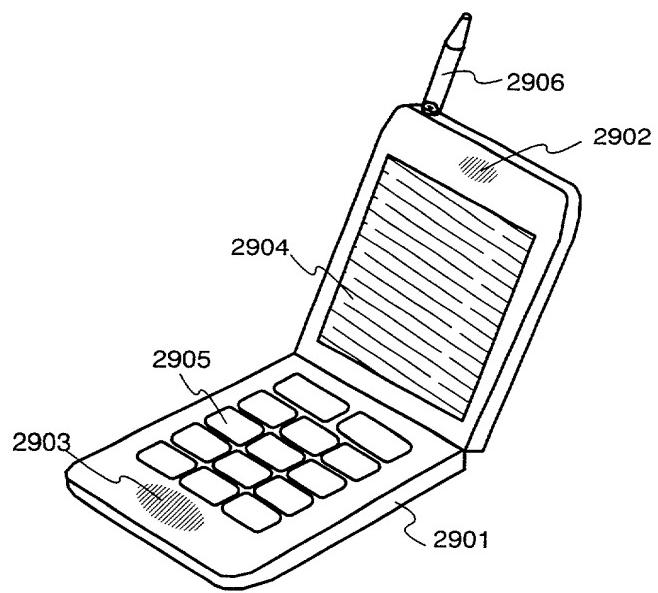


Fig. 28A

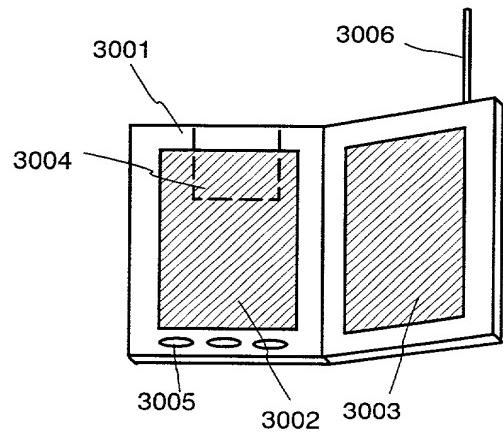


Fig. 28B

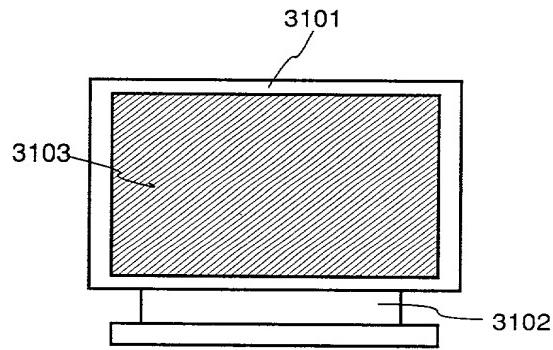


Fig. 28C

Fig. 29

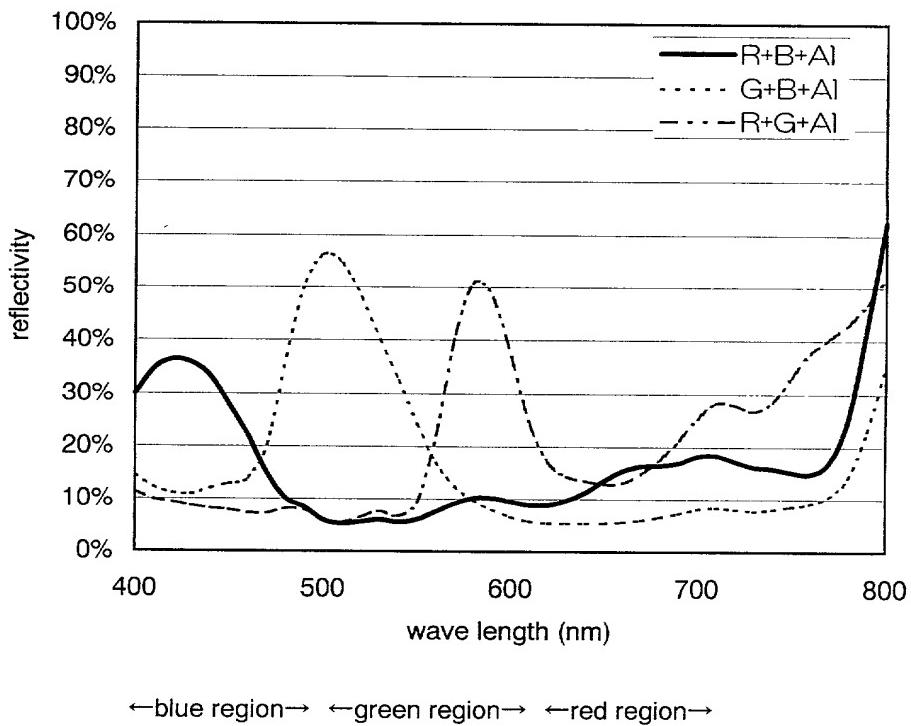


Fig. 30 prior art

